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Interview  
Questions  
Answers

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Questions and Answers  
for 17.Dec.2020 to  
crack your Digital Logic  
Design interview.

## **Top 28 Digital Logic Design Interview Questions and ...**

250+ Digital Logic

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Interview

Questions and

Answers, Question1:

Explain about setup  
time and hold time,  
what will happen if  
there is setup time and  
hold time violation, how  
to overcome this?

Question2: What is  
skew, what are  
problems associated  
with it and how to  
minimize it? Question3:  
What is slack?

Question4: What is  
glitch?

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## **TOP 250+ Digital Logic Design Interview Questions and ...**

Digital Electronics  
Interview Questions for  
beginners and  
professionals with a list  
of top frequently asked  
Control Systems  
interview questions  
and answers with java,  
... The basic gates that  
make up the digital  
system are called a  
logic gate. These are

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Interview  
Questions

easy to design. These  
are difficult to design.

## 3. Answers

### **Top 39 Digital Electronics Interview Questions - javatpoint**

There are no predefined Digital design interview questions as the person can ask anything starting from a simple concept to advance level and it also varies at different



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Interview  
Questions  
Answers

experience level. So it's better to get prepared all the concepts. However in general some of the questions and topics which are important are:-1] Number System:-

## **What are Digital Logic Design interview questions?** **- Quora**

the disc is white and remaining is black.  
When black portion is

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## Interview

under sensor it generates logic 0 and logic 1 when white portion is under sensor. Design Direction finder block using digital components (flip flops and gates) to indicate speed. Logic 0 for clockwise and Logic 1 for counter clockwise.

Q Page 4 of 9

3/20/2006

## **Sample Questions asked in Interviews**

Find out the best

*Page 10/27*

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Interview  
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answers to Digital  
Design Interview  
Questions prepared by  
experts helps you to  
clear your upcoming  
interviews on Digital  
Design. ... This is an  
efficient way of  
encoding Boolean logic  
functions, and LUTs  
with 4-6 bits of input  
are in fact the key  
component of modern  
FPGAs. Q3.

## **Digital Design Interview Questions**

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## Interview Questions and Answers | Basic and ...

Digital Design  
Interview Questions -  
All in 1 ... Useful  
information is shared in  
this blog. these digital  
design questions are  
helpful for improving  
basic ... particularly if  
the reset is gated with  
the logic generating  
the d-input. But in such  
a case, the  
combinational logic  
gate count grows, so  
the overall gate count

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Interview  
savings may not be ...

## Questions

### **Digital Design Interview Questions - All in 1**

Let us move to the next Digital Electronics Interview Questions.

Q4. Why most interrupts active Low?

Answer: The reason for most signals to be active low is as below:

If you consider transistor as an example, active low means the capacitor in

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Interview  
Questions  
Answers

transistor output terminal will get charged or discharged based on signals from low to high or from high to low transitions respectively.

## **10 Essential Digital Electronics Interview Questions ...**

15. Which of the following gate is a two-level logic gate (a) OR gate (b) NAND gate (c) EXCLUSIVE OR gate (d) NOT gate. Ans:c.

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## Interview DIGITAL ELECTRONICS

Interview Questions ::

16. Among the logic families, the family which can be used at very high frequency greater than 100 MHz in a 4 bit synchronous counter is (a) TTLAS (b) CMOS (c)ECL (d)TTLLS

Ans ...

## **300+ TOP DIGITAL ELECTRONICS Questions and Answers Pdf**

For a state machine

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Interview Questions Answers

with 9- 16 states, a binary FSM only requires 4 flip-flops while a one hot FSM requires a flip-flop for each state in the design FPGA vendors frequently recommend using a one hot state encoding style because flip-flops are plentiful in an FPGA and the combinational logic required to implement a one hot FSM design is typically smaller than most binary encoding



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## Questions

### **TOP 250+ Digital Electronics Interview Questions and ...**

Digital design interview questions & answers.

1) Explain about setup time and hold time, what will happen if there is setup time and hold time violation, how to overcome this? Set up time is the amount of time before the clock edge that the input signal needs to

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Interview  
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Answers

be stable to guarantee  
it is accepted properly  
on the clock edge.

## **Digital design interview questions & answers**

analog and digital  
electronics mcq analog  
and digital electronics  
mcq pdf analog and  
digital electronics mcqs  
analog and digital  
electronics objective  
questions and ...

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interview questions**

**Answers**

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RTL and Verilog  
Interview ... "This is a  
great book with lots of  
information and  
material about Verilog  
and digital design. The  
book is packed with  
questions and  
drawings, figures,  
timing diagrams and  
example code.

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## **Digital Logic RTL and Verilog Interview Questions**

Welcome to Interview Questions Section on FullChipDesign. Getting through interviews is always a challenging task and requires thorough preparation. Here is a list of probable questions that may appear in an interview related to RTL skills. Three areas are covered Digital

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Interview  
Questions  
Answers  
Design, Digital  
Fundamentals and  
FPGA Design.

## **Welcome to Interview Questions Section on FullChipDesign.**

Digital electronics is also that branch of science that uses fibre optics to detect digital display. Most of the reputed electronics companies put up their questions from this portion of electronics.

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Interview  
Questions  
Answers

So to assist the aspirants, we are providing here some frequently asked interview questions and answers on digital electronics

## **Interview and viva questions and answers on Digital**

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Digital Logic Questions  
Digital Logic Questions  
This contains a sample list of questions related to Digital Logic Design.

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Interview Questions Answers  
Since the job of a verification engineer is to verify the functional correctness of Digital Logic, this section forms a crucial part of any interview process. This is never a complete list, but try to focus your ...

## **Sample Questions in Digital Logic Design**

1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the

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Interview  
Questions  
Answers

true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

## **Top 17 VLSI Interview Questions & Answers**



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Last Minute Notes (LMNs) Quizzes on Digital Electronics and Logic Design; Practice Problems on Digital Electronics and Logic Design ! Please write comments if you find anything incorrect, or you want to share more information about the topic discussed above.

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*Page 25/27*

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Interview

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Digital Design Engineer at Cirrus Logic was asked... Nov 4, 2011.  
Verilog question with additional requirements.

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